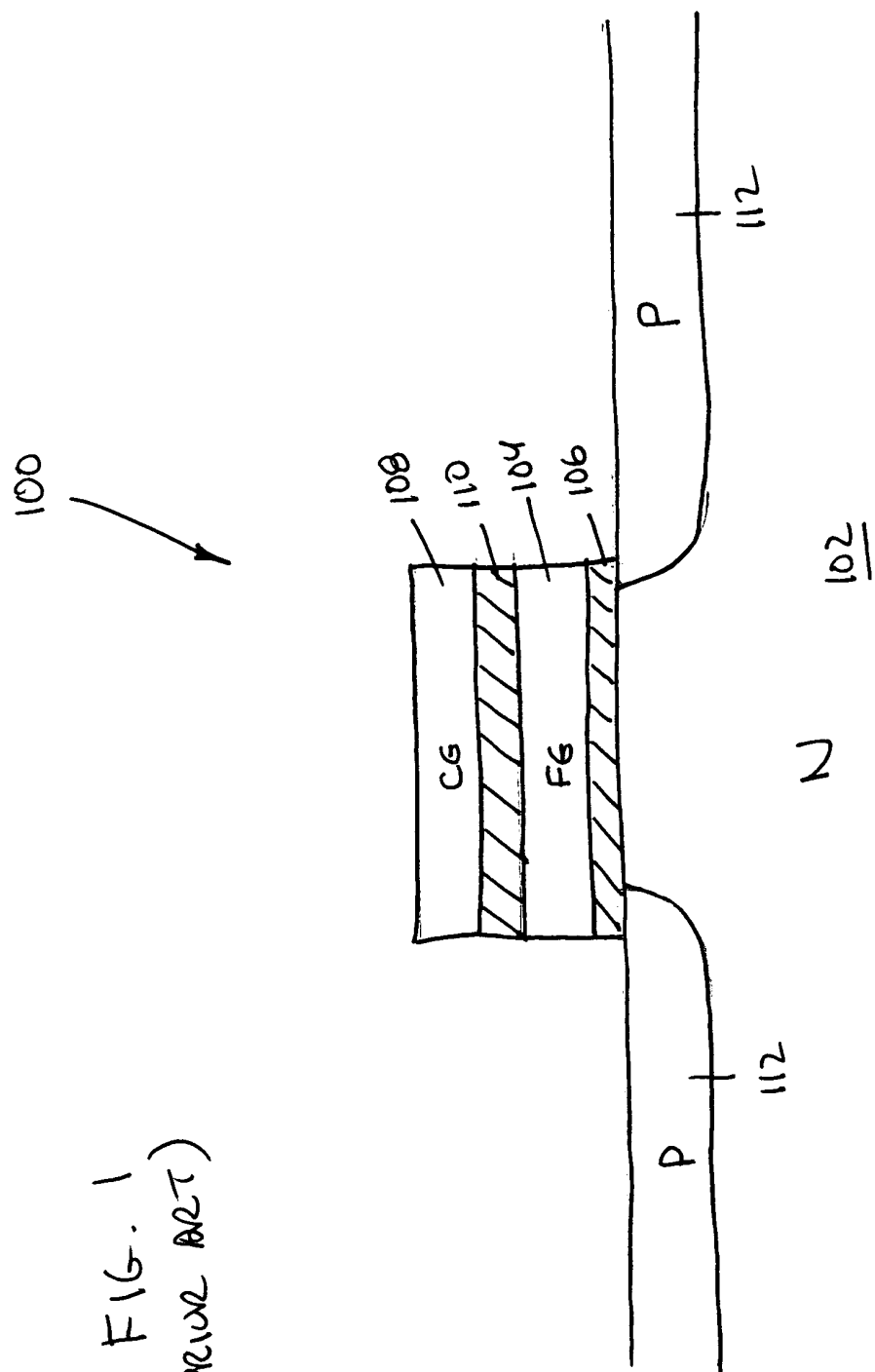


FIG. 1
(PRIOR ART)



PMOS Gate Current vs. Gate Voltage

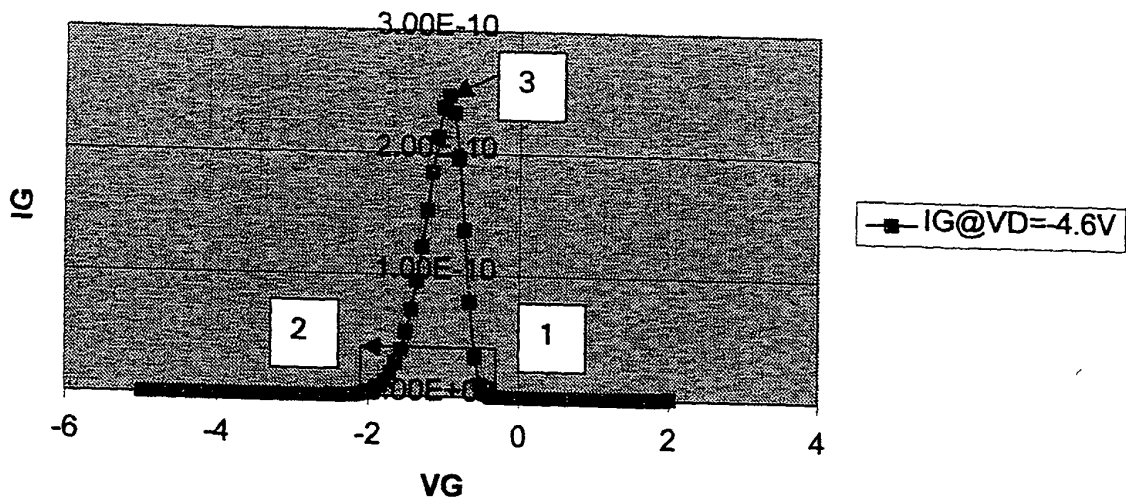


FIG. 2

PMOS Substrate Current vs. Gate Voltage

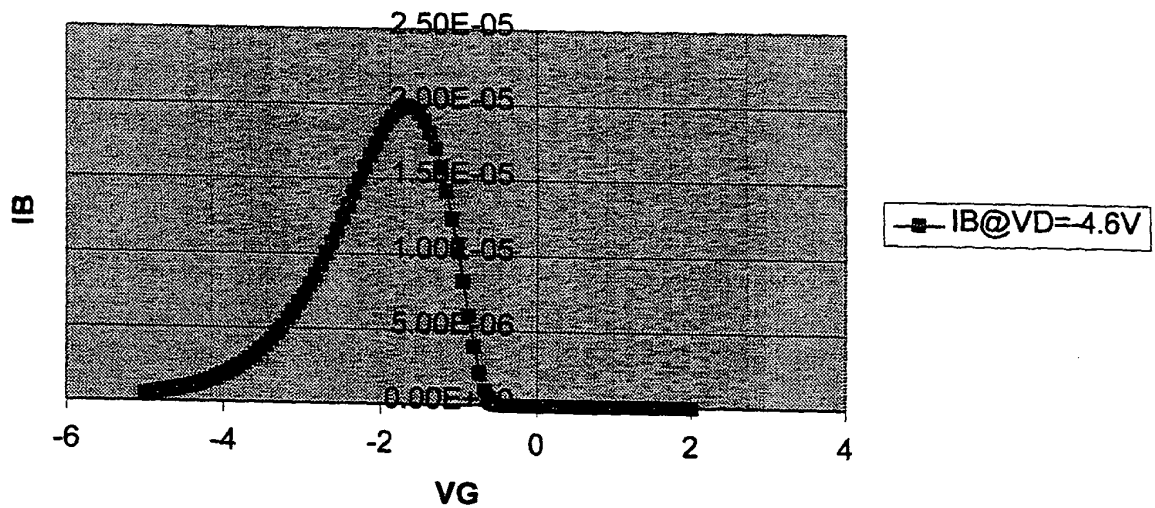


FIG. 3

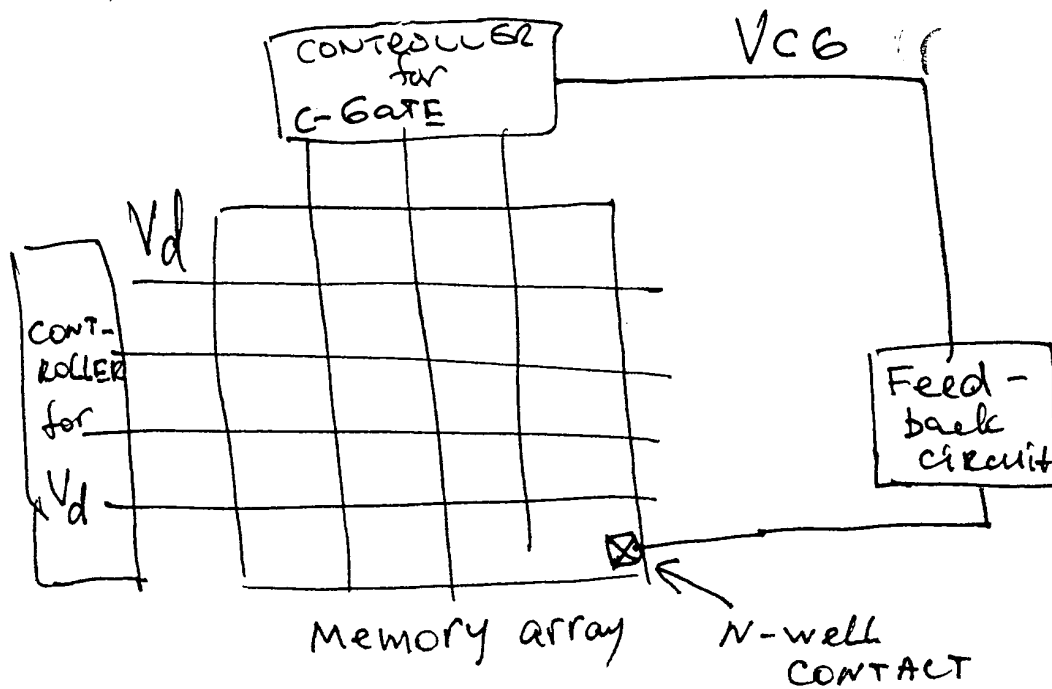


FIG. 4A

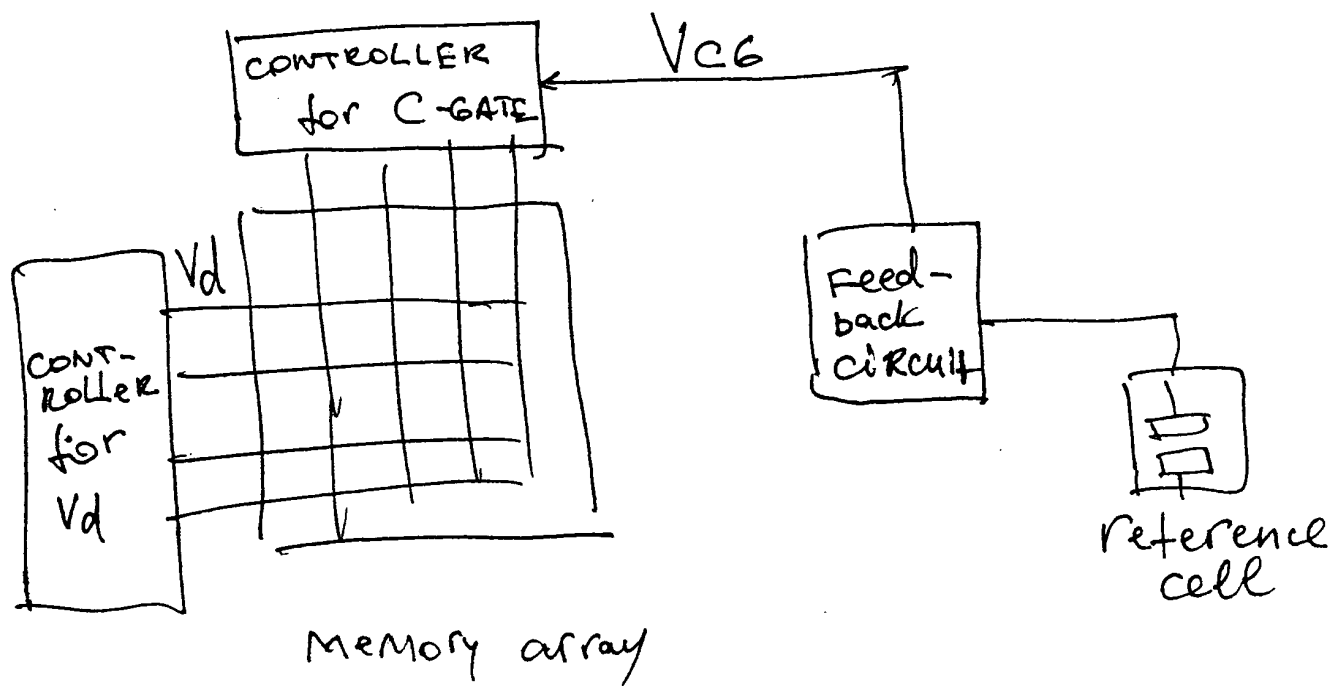


FIG. 4B